

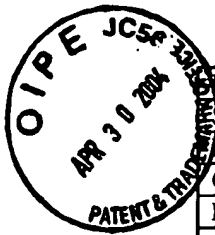
First Named Inventor	Chun Chen	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
Serial No.	10/662,074	
Filing Date	September 12, 2003	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Confirmation No.	9792	
Attorney Docket No.	400.181US03	
Title: CONTACTLESS UNIFORM TUNNELING SEPARATE P-WELL (CUSP) NON-VOLATILE MEMORY ARRAY ARCHITECTURE, FABRICATION AND OPERATION		

U.S. Patent References				
Examiner Initials	Document No.	Issue/Published Date	Name	Filing Date
W	5,343,423	08/30/94	Shimoji	12/23/91
W	5,691,938	11/25/97	Yiu et al.	05/03/94
W	6,137,723	10/24/00	Bergemont et al.	04/28/99
W	6,181,601 B1	01/30/01	Chi	12/02/99

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	T?
	Country	No.			

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
Z	S. Aritome, et al. "A reliable bi-polarity write/erase technology in flash EEPROMS", Int'l. Electron Devices Meeting, 1990, <u>Technical Digest</u> , 9-12 Dec. 1990; pp. 111-114.

Examiner Signature	<i>K. P. ...</i>	Date Considered	9/13/04
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Examiner Signature	<i>H. Picard</i>	Date Considered	9/13/04
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JP	5,945,698	08/31/99	Prall	07/01/97
	5,973,352	10/26/99	Noble	08/20/97
	5,973,356	10/26/99	Noble	07/08/97
	6,080,672	06/27/00	Juengling	08/20/97
	6,160,277	12/12/00	Prall	03/19/99
	6,191,444 B1	02/20/01	Clampitt	09/03/98
	6,218,698 B1	04/17/01	Hai	08/17/98
	6,272,047 B1	08/07/01	Mihnea	12/17/99
	6,319,774 B1	11/20/01	Hai	02/27/98
	6,337,244 B1	01/08/02	Prall	03/01/00
	6,406,959 B2	06/18/02	Prall	01/04/99
	6,441,428 B1	08/27/02	Ghods	03/19/01
	6,489,665 B2	12/03/02	Prall	12/20/00
	2002/0044483 A1	04/18/02	Prall	08/28/01
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Examiner Signature	<i>JP Prall</i>	Date Considered	9/13/04
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Examiner Signature	<i>R. Prall</i>	Date Considered	9/15/04
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